



NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA

[An Institute of National Importance under Ministry of Education, Govt. of India]

Sector 1, Rourkela, Sundergarh Dist., Odisha 769 008

SPONSORED RESEARCH INDUSTRIAL CONSULTANCY CONTINUING EDUCATION

NITR / SR / 2025 / Advt.- 25EC051 / L / 118

dt.09-Feb-2026 Ref: FTS 260205-5011

SR-R03

Advertisement for Recruitment of Project Position

Candidates who fulfill the below mentioned criteria may submit the application form before the last date. Engagement will be purely on temporary / contractual basis and co-terminus with the completion of the project. Candidates are advised to go through the advertisement details carefully before applying.			
1.	Title of the Project:	Design and Fabrication of Signal Conditioning ASIC for MEMS Accelerometer	
2.	Project Code, Dept. with Closing Date:	SR/25/EC/051	02-Nov-2027 Electronics and Communication Engg.
3.	Funding Agency Details:	RCI, DRDO, Hyderabad	
4.	PI details:	Prof. Sougata Kar 9433280223 kars@nitrrkl.ac.in, 0661-246 2473 sougatakar@gmail.com	
5.	Co-PI details:	Prof. - - @nitrrkl.ac.in, 0661-246 - - @gmail.com	
6.	Details of the Post(s):		
Project Assistant (PrA) [Name of the Post(s)]		7. Educational Qualification & Working Knowledge	
01 (One) [No. of Post(s)]	02 Year(s), 00 Month(s) (Tenure of Post)	Essential Qualification (s): B.E./B. Tech. in Electronics/Electrical/Instrumentation or similar fields with above 60% marks OR M.E./M. Tech. in Electronics/Electrical/Instrumentation or similar with specialization in VLSI/Instrumentation/Electronics/Electrical or similar with above 60% marks.	
For the Tenure as Mentioned above		Desirable Qualification:	
INR 20,000.00 /- per month (+) HRA @ NA % (if applicable)		GATE score is not mandatory but desirable. Previous experience in Analog IC design in Cadence and embedded systems design with micro-controllers are desirable.	
- NA -			
INR 0.00 /- per month (+) HRA @ NA % (if applicable)			
Job Description:			
The project students will be involved in the design of analog integrated circuits for sensor interfacing for MEMS capacitive accelerometer. They have to work on Cadence for circuit simulation for analog integrated circuit and implementation of SC based circuits, amplifier, filter, sigma-delta ADC etc. to form a complete interfacing ASIC. Project scholar with B.E./B.Tech. may enroll in M. Tech. (R) satisfying institute rule.			
Notified Later	Interview details:	-	Department of Electronics and Communication Engg.
Application link for eligible candidate(s): NIT Rourkela Homepage ➡ FACULTY & STAFF ➡ SRICCE ➡ Career ➡ Notices			
The candidate(s) are required to send the complete filled and signed application (soft copy) with documents regarding educational qualification indicating percentage of marks / division (mark-sheets and / or certificates), research papers (if any), work experience certificate (if any) etc., This may be built as a single PDF file and sent by email with "Advertisement No." on the subject link to the above mentioned e-mail IDs. NO hard copies of application(s) are required to be sent to the Institute. Last date for submitting the Application: 23-Feb-2026			
The period of experience in a discipline / area of work, wherever prescribed, shall be counted after the date of acquiring the minimum prescribed educational qualifications for that position. Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project. Selection / Joining will be cancelled in case of any suppression of information / document submitted.			
NIT Rourkela reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post. Only short-listed candidates will be informed for Online interview. In case, any clarification is required on eligibility regarding the above post, the candidate may contact in the above mentioned details.			
Age Guideline: The upper age limit for applying for the award of project position shall be 28 years, which is relaxed up to 5 years in the case of candidates belonging to Schedule Castes / Schedule Tribes / PWD and Female applicants whereas 3 years in the case of OBCs (Non-creamy layer candidates). Upper age limit shall be reckoned as on the last date of receipt of applications.			
Any other terms & conditions governed as per guidelines issued by the funding agency for the engagement of above position as amended from time to time shall be in force towards this temporary recruitment.			
Sd/- Asst. / Dy. / Jt. Registrar (SR)			
Copy to:			
➤ PI & CO- PI: Prof. Sougata Kar, EC & Prof. - ➤ Chairman, DRC, Dept. of EC			
➤ Head of the Department / Centres / Units (It is requested that the contents of the above advertisement be brought to the notice of the staff(s) / student(s) working in your Deptt. / Centre / Unit.)			
➤ Dealing Seat (SR – Project Recruitment) ➤ Advertisement File ➔ To publish advertisement at NITR website.			